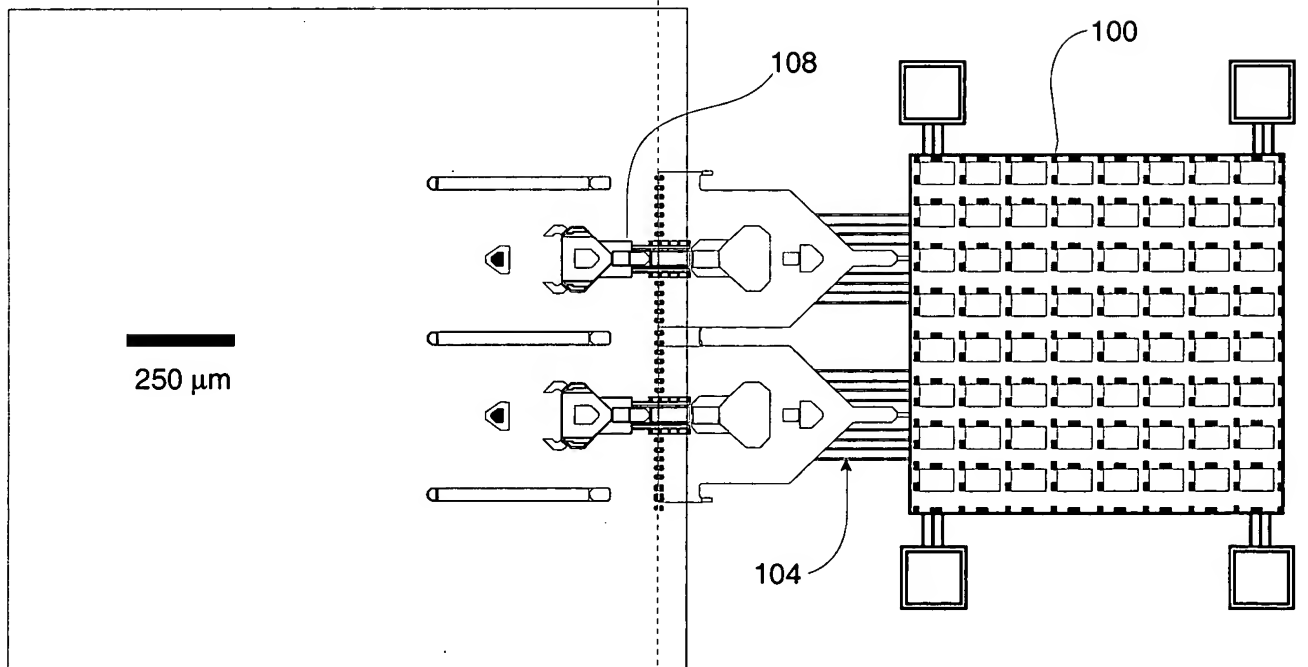
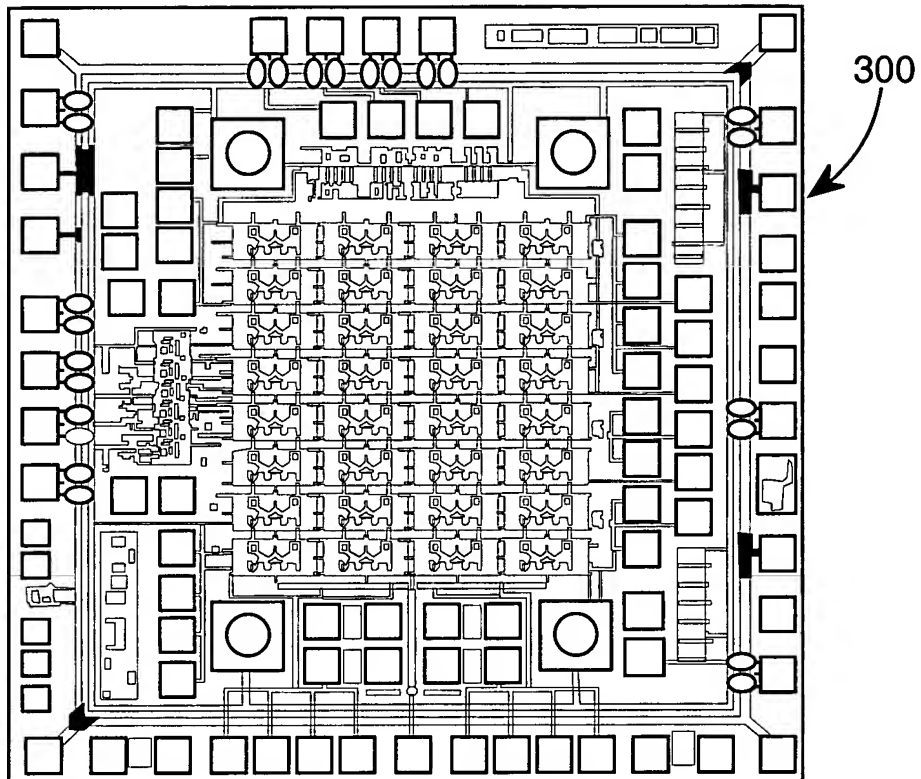


*Fig. 1*



*Fig. 2*



*Fig. 3*

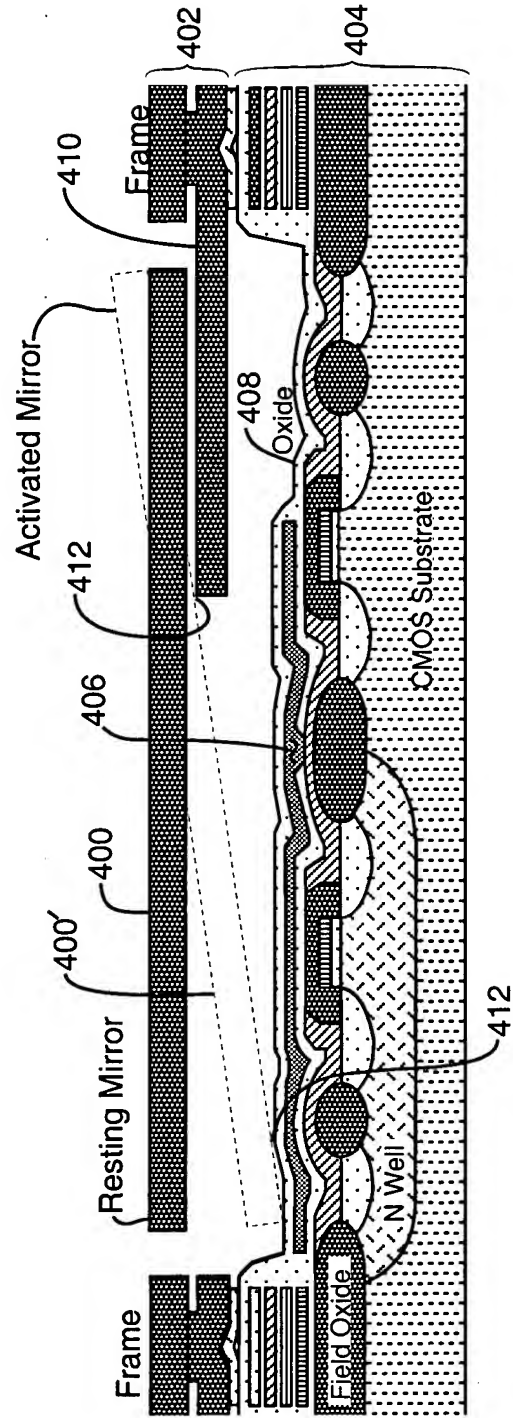
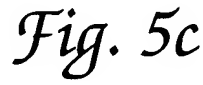
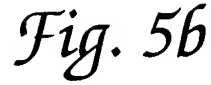
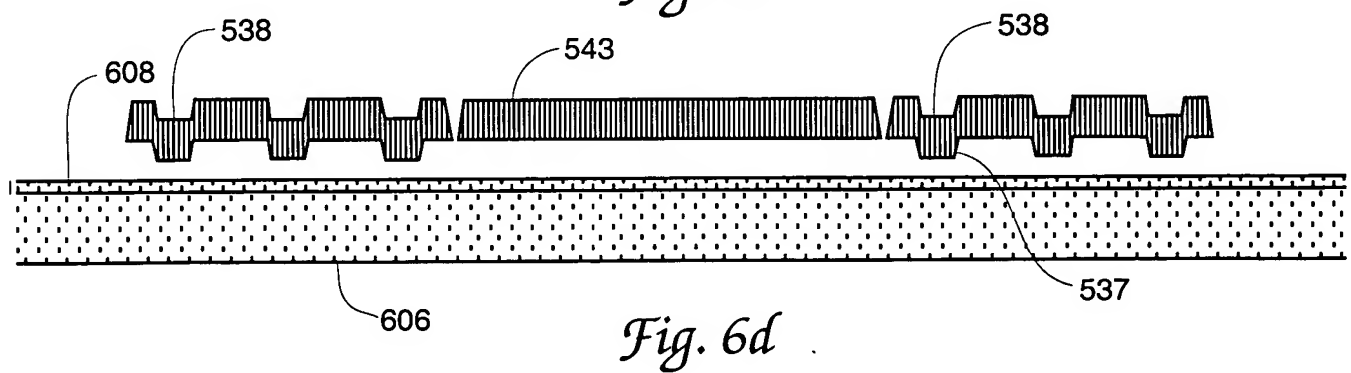
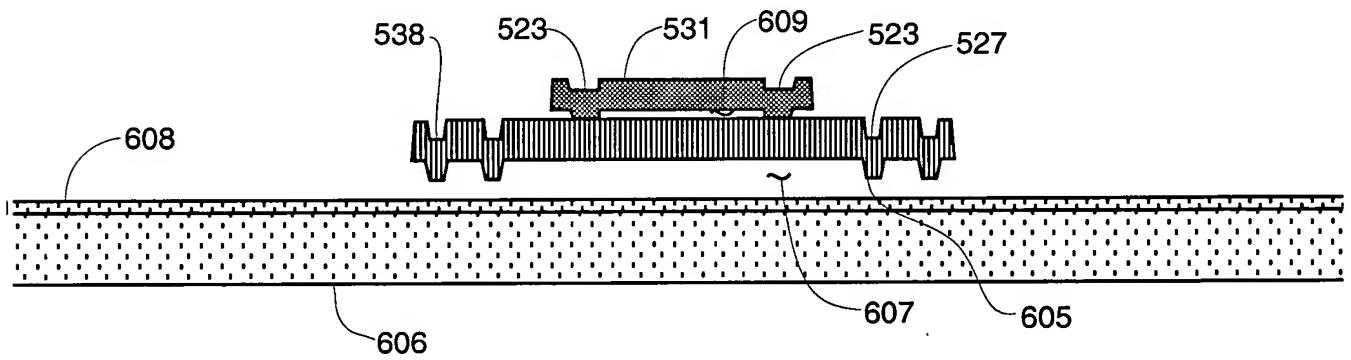
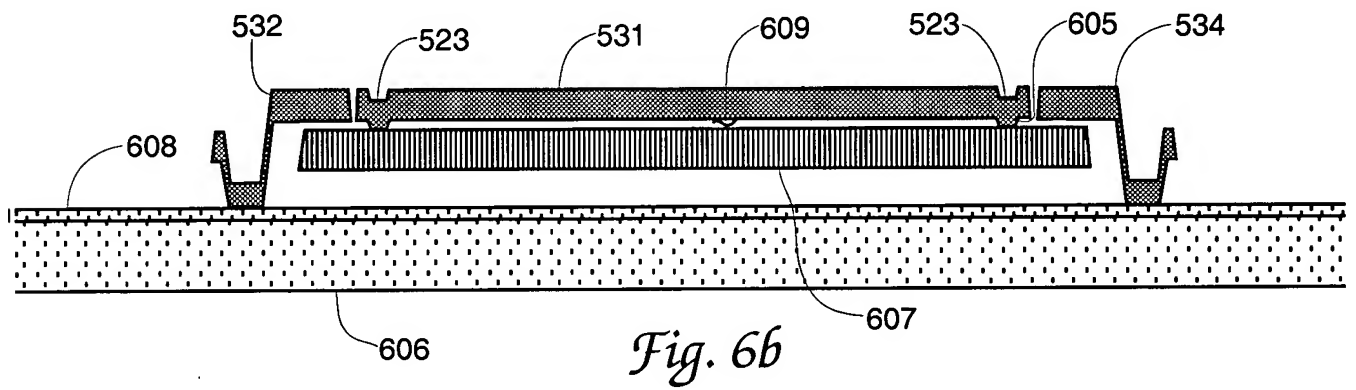
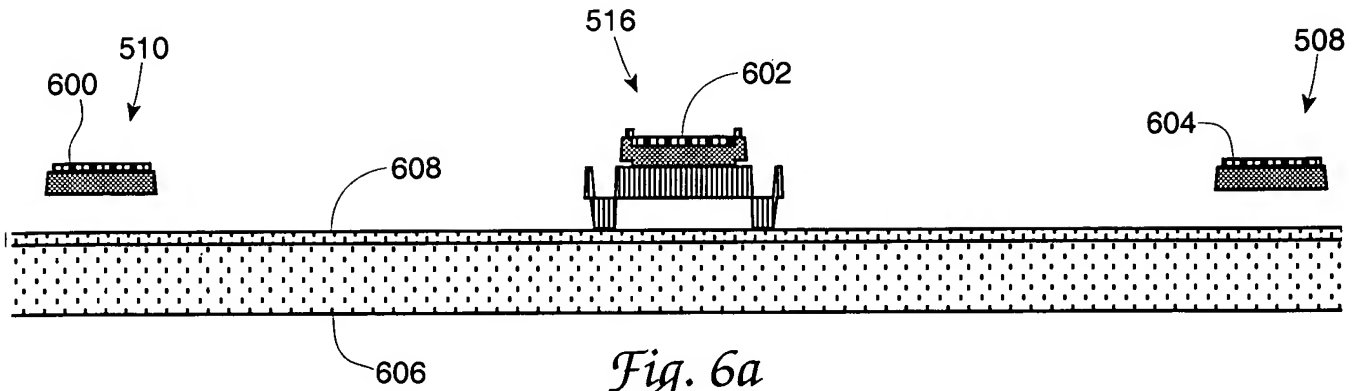
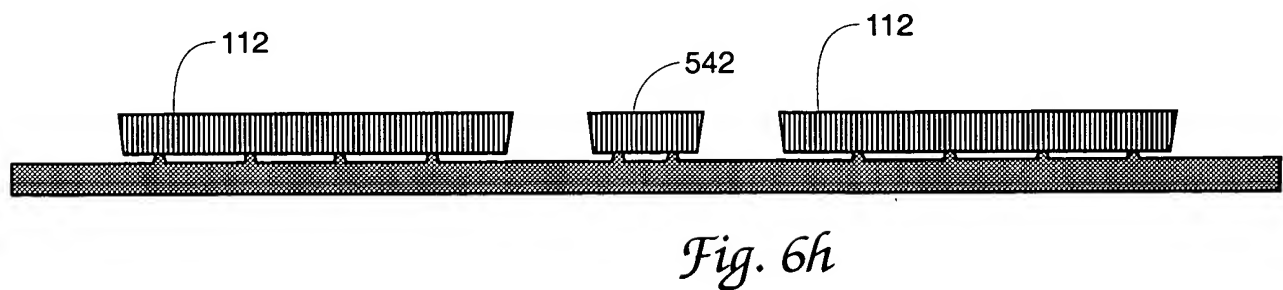
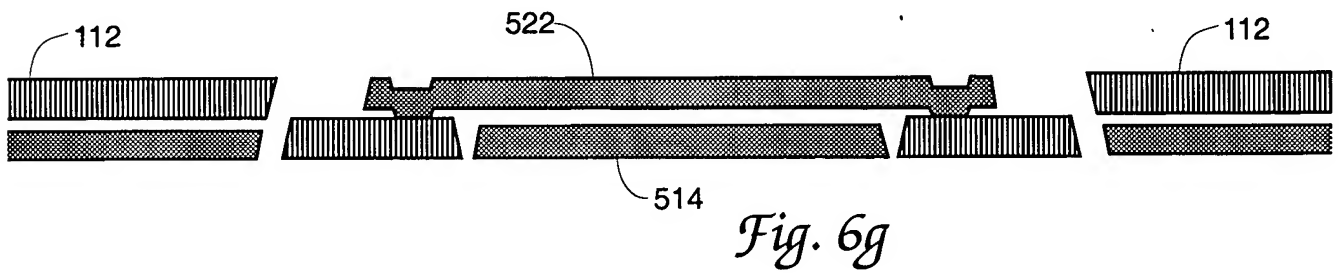
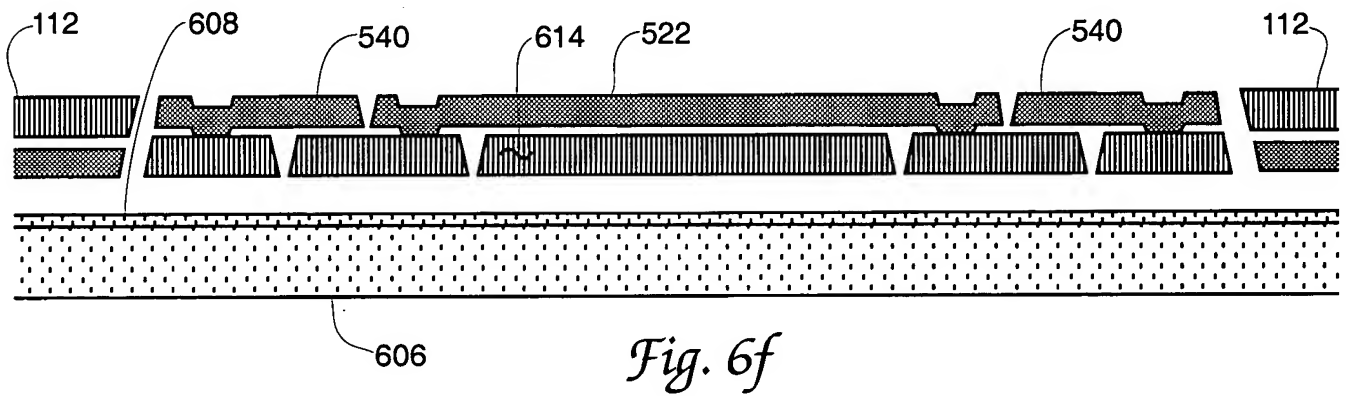
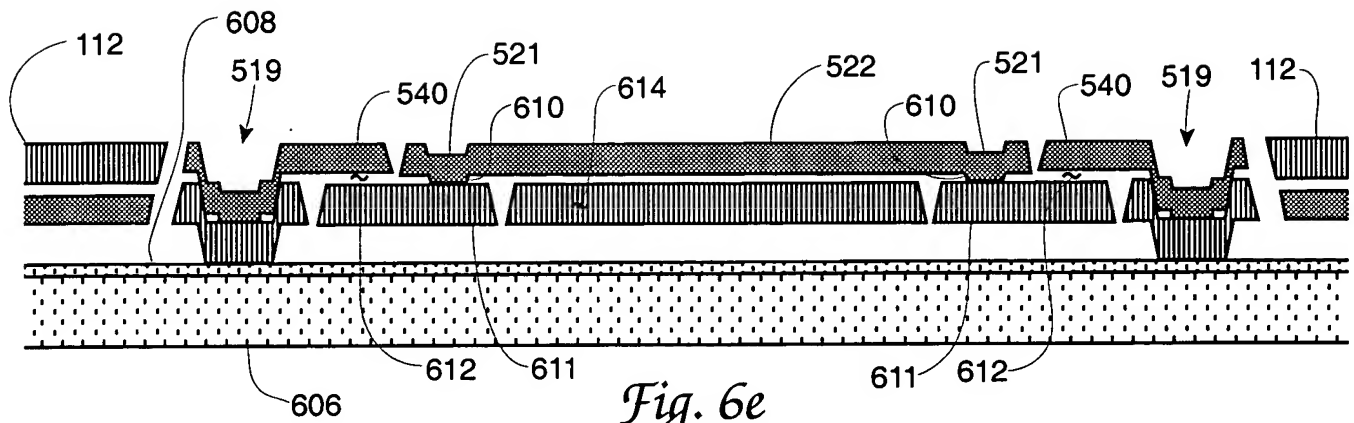


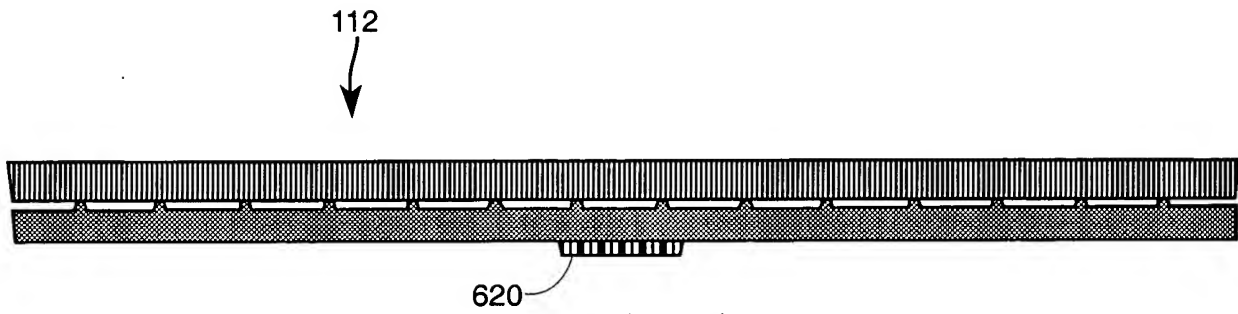
Fig. 4



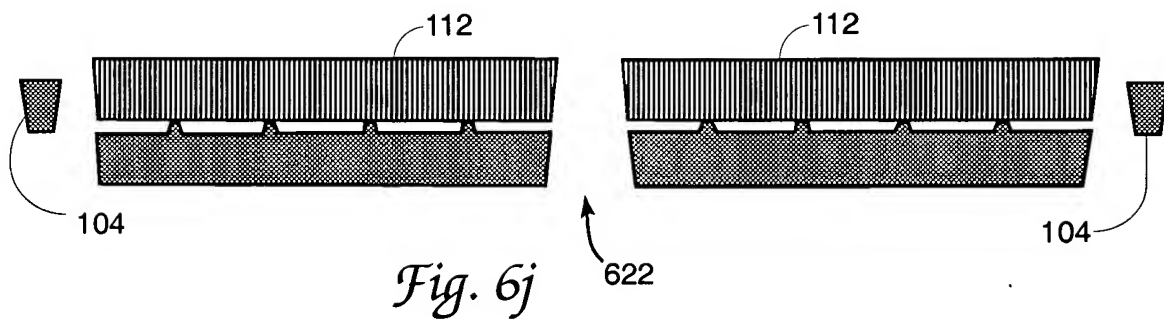
Flip-Chip Structures Intended for Integration



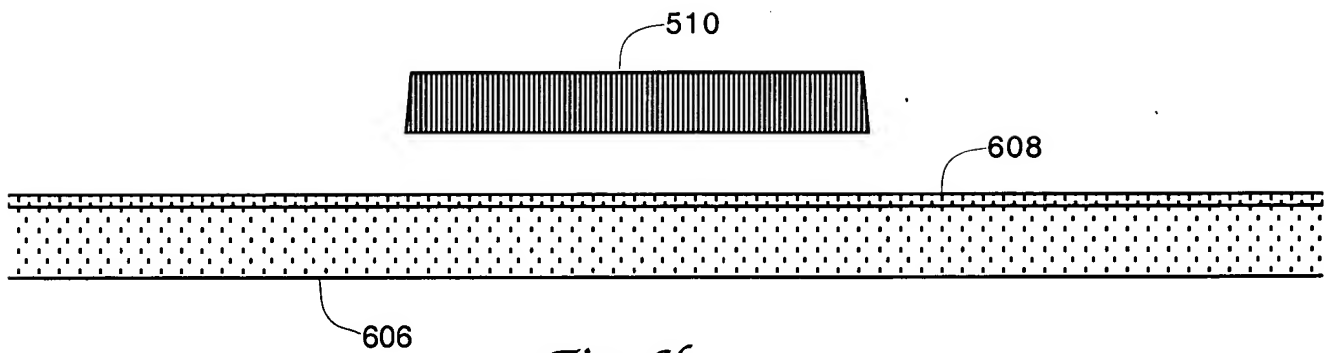




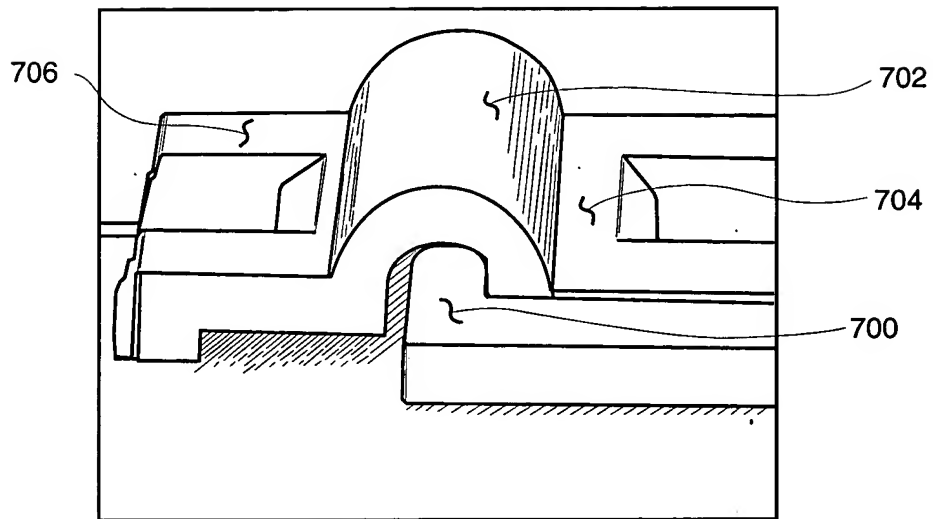
*Fig. 6i*



*Fig. 6j*

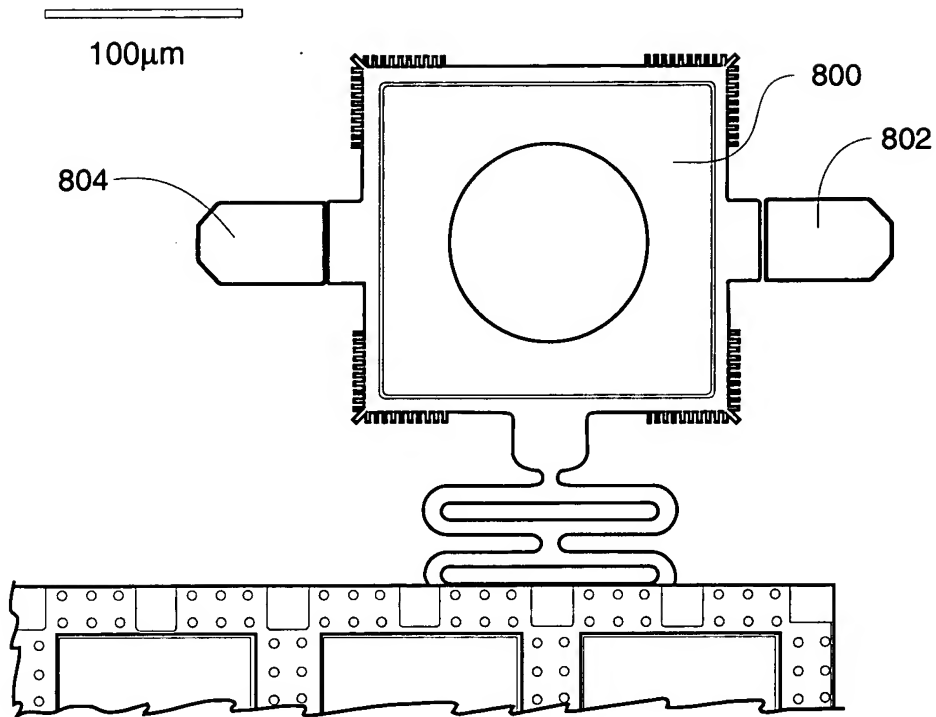


*Fig. 6k*

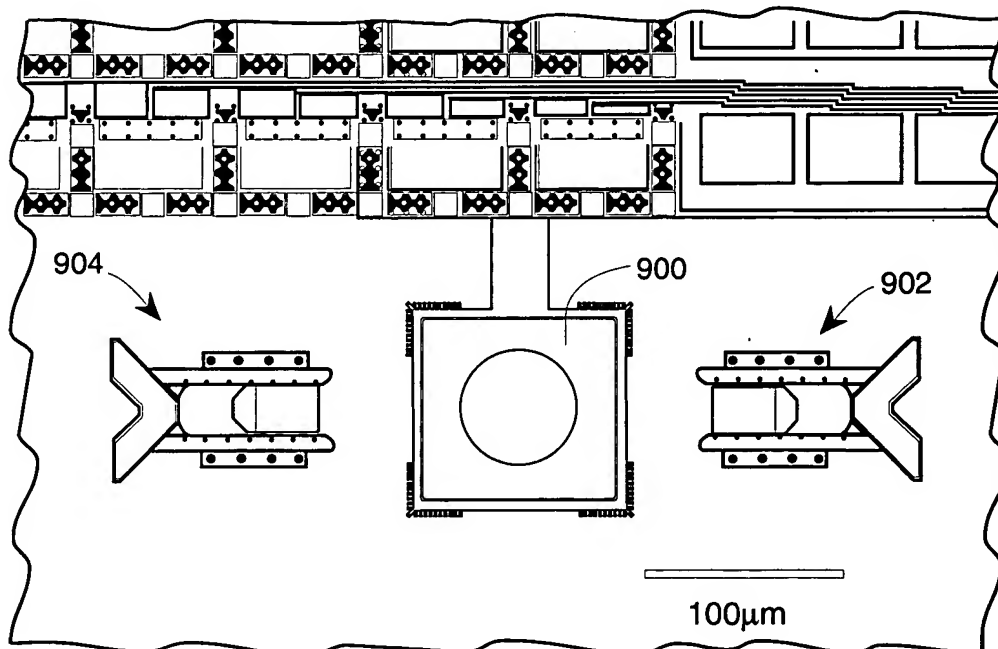


*Fig. 7*

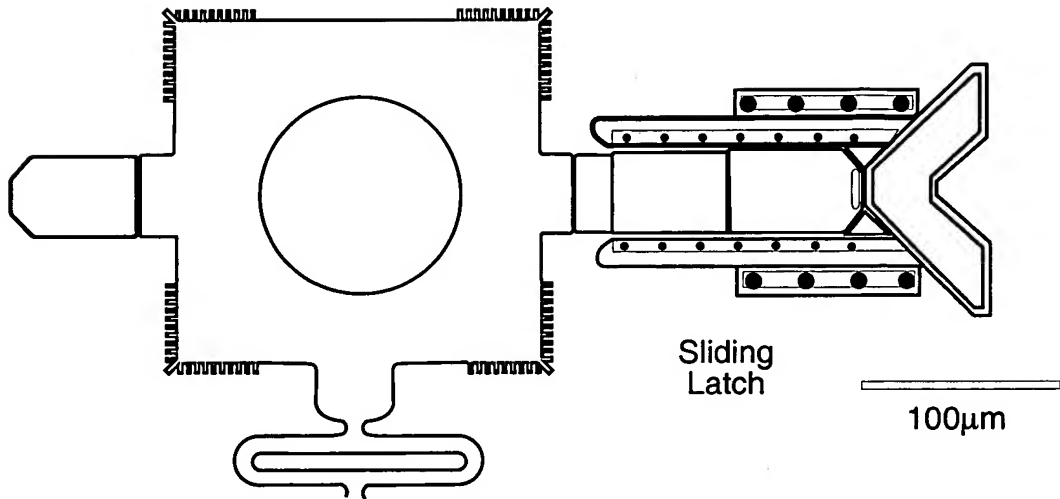




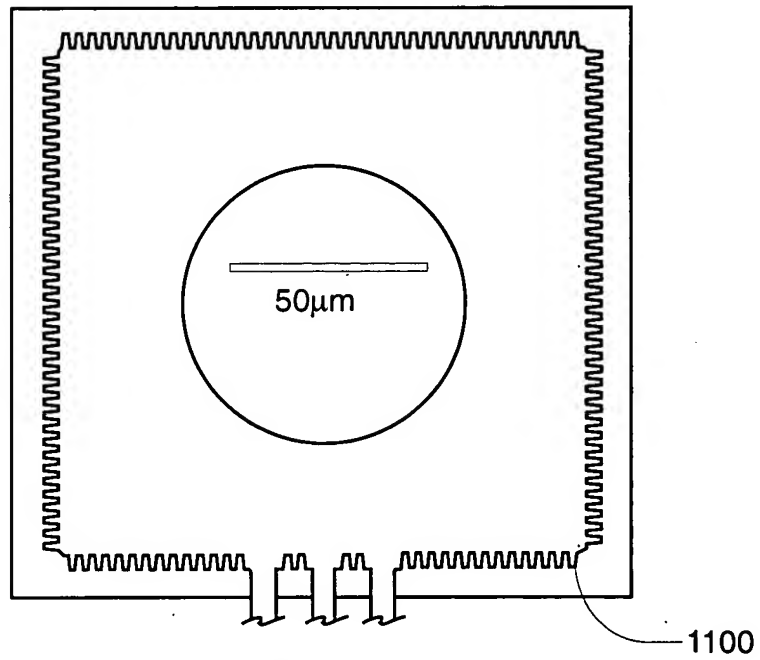
*Fig. 8*



*Fig. 9*



*Fig. 10*



*Fig. 11*